Applicant: Sung-We Cho   Serial No.   ~~not yet assigned~~ *10/674847*

Filing Date: September 29, 2003   Group: ~~not yet assigned~~ *2816*

Title: MTCMOS FLIP-FLOP CIRCUIT CAPABLE OF RETAINING DATA IN SLEEP MODE

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub Class
—	—					

FOREIGN PATENT DOCUMENTS

Exam Init	Ref	Document Number	Publication Date	Country	Name
—	—				

OTHER DOCUMENTS

Exam Init	Ref	Author, Title, Date, Pertinent Pages, Etc.)
✓	—	IEEE JOURNAL OF SOLID STATE CIRCUIT, Vol.32, No.6, Satoshi Shigematsu, 1997, entitled A 1-V High-Speed MTCMOS Circuit Scheme for Power-Down Application Circuits.

Examiner: _____

Date Considered: __8/3/2004__